

Handheld Computing System 110

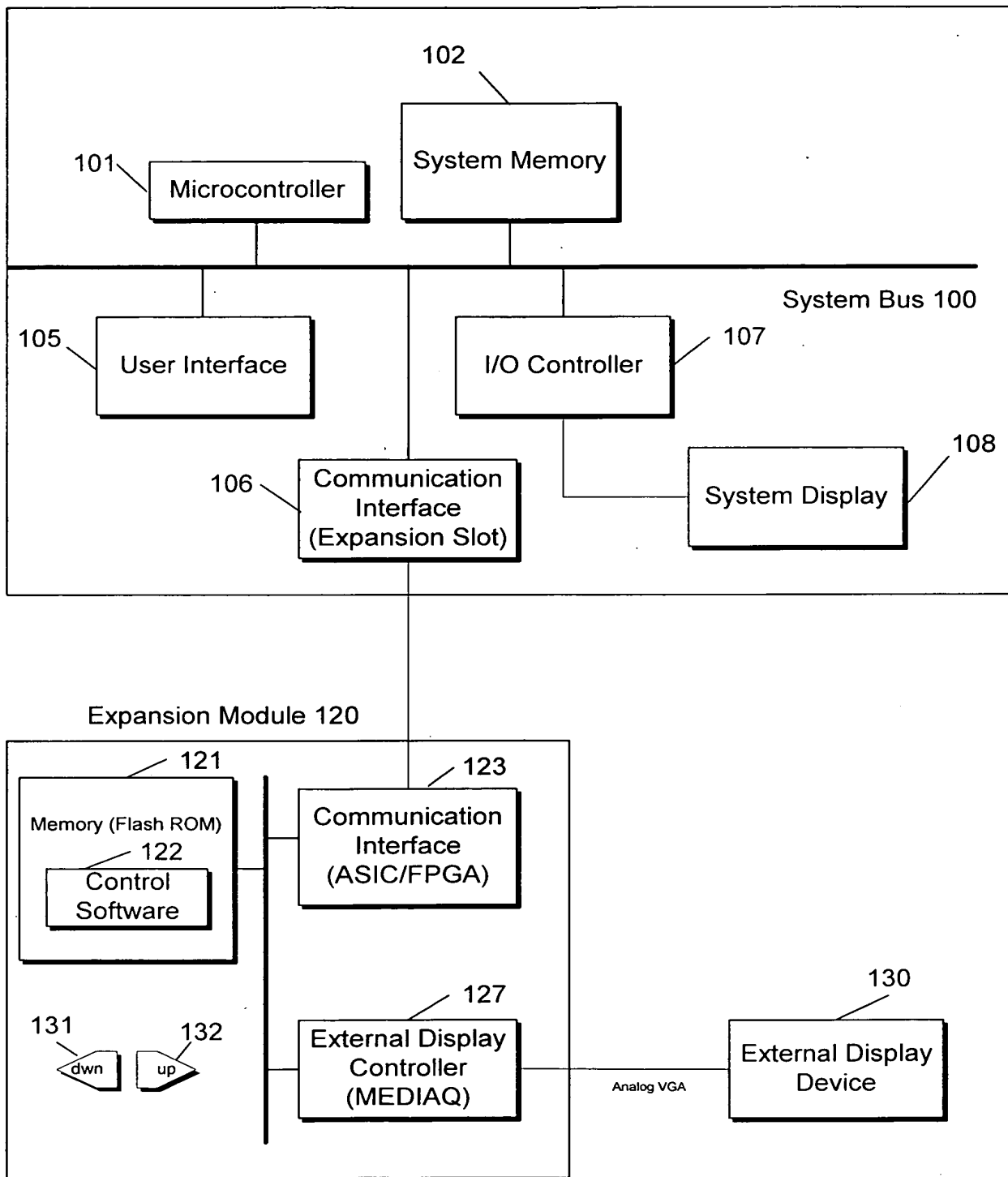


FIG. 1

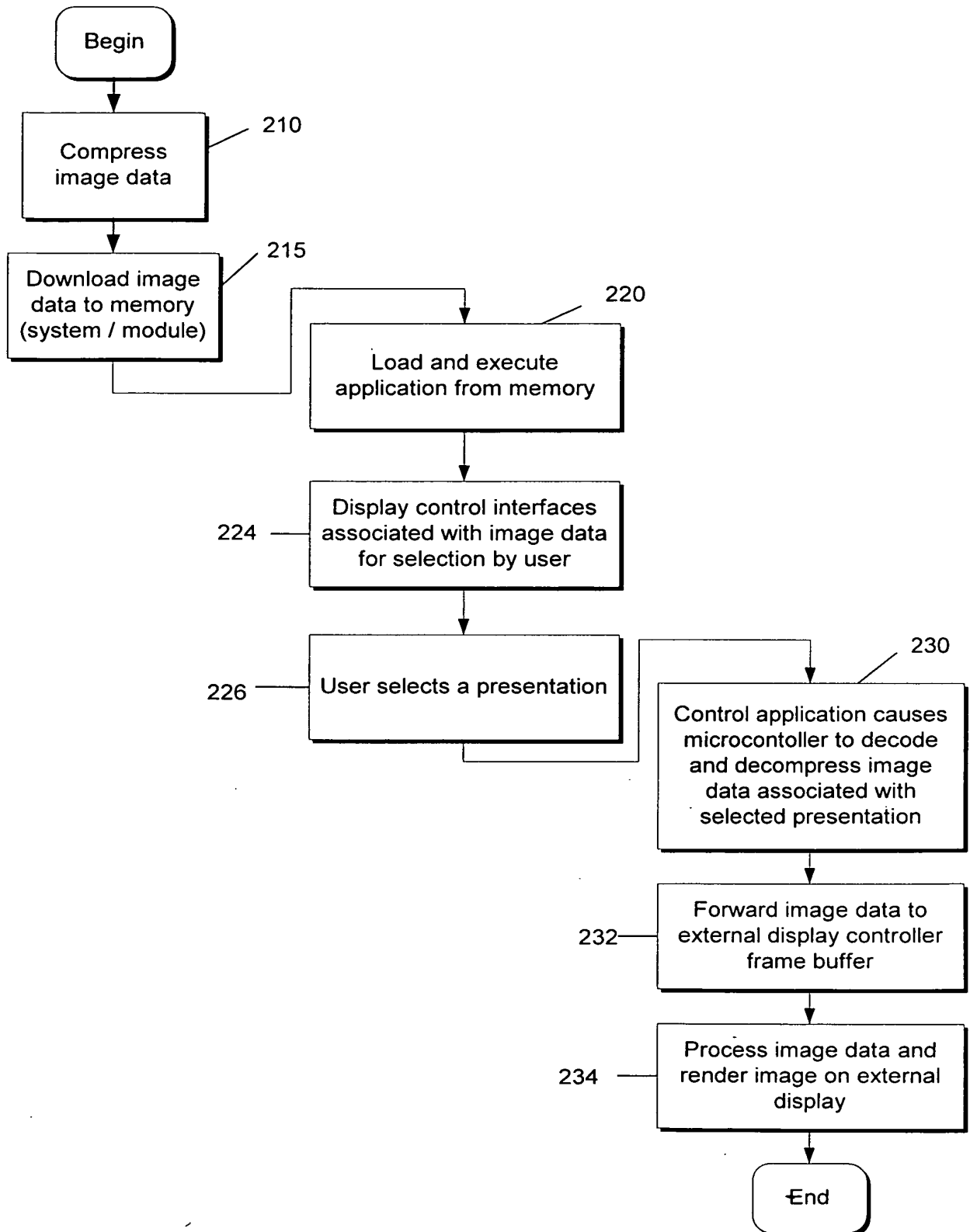


FIG. 2

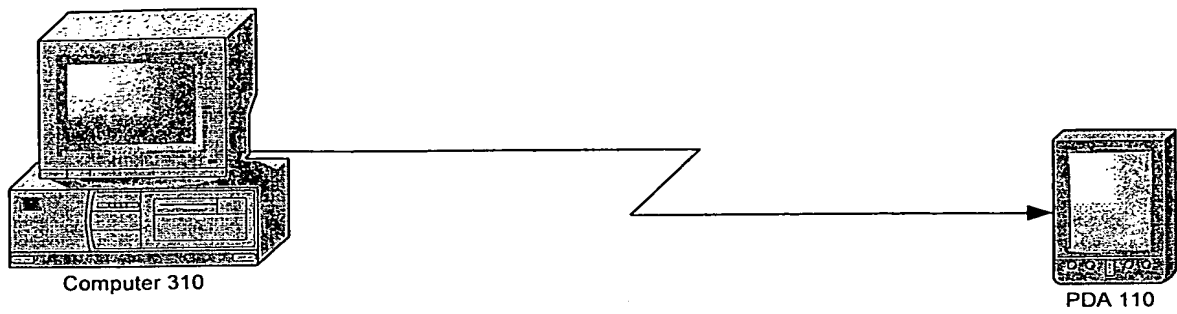


FIG. 3A

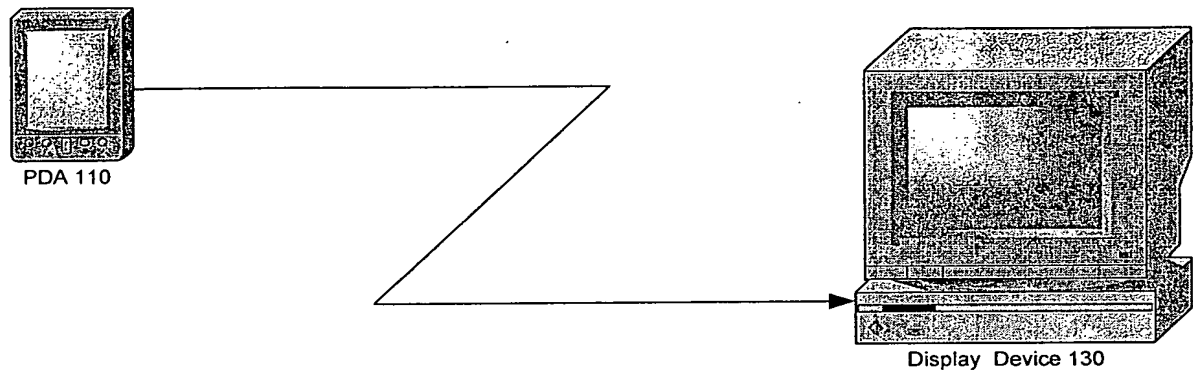


FIG. 3B

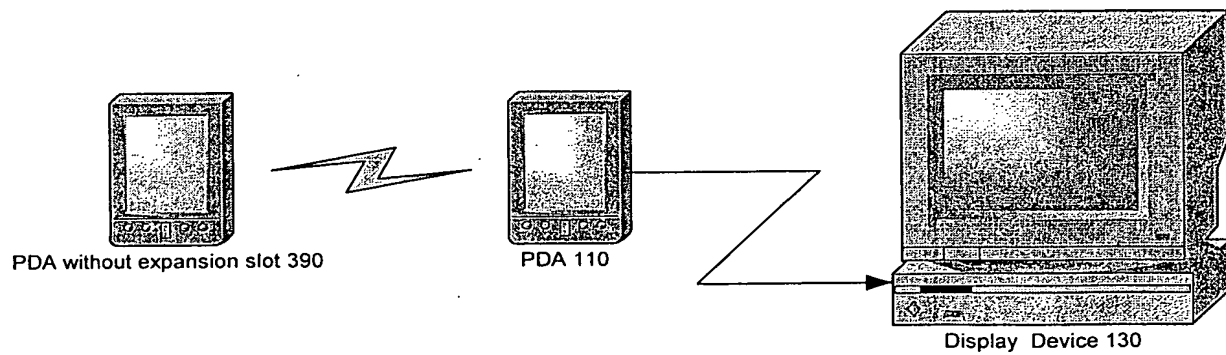


FIG. 3C

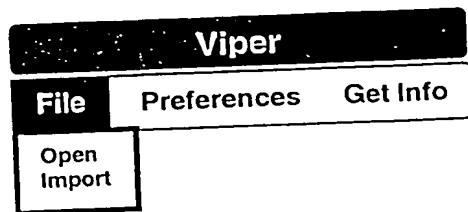


FIG. 4A

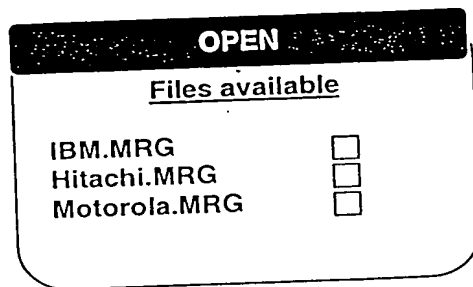


FIG. 4B

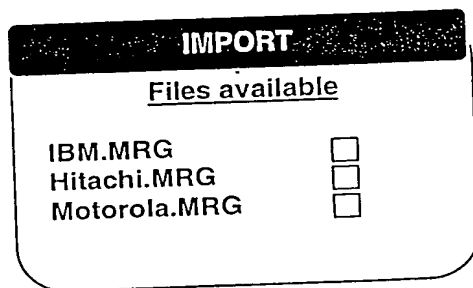


FIG. 4C

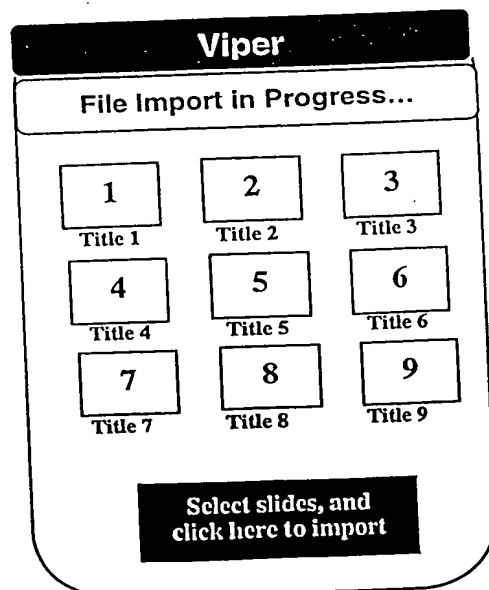


FIG. 4D

FIG. 4E

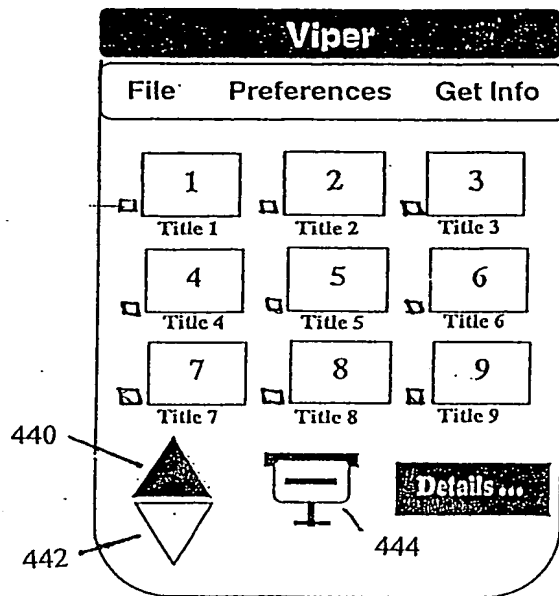


FIG. 4E

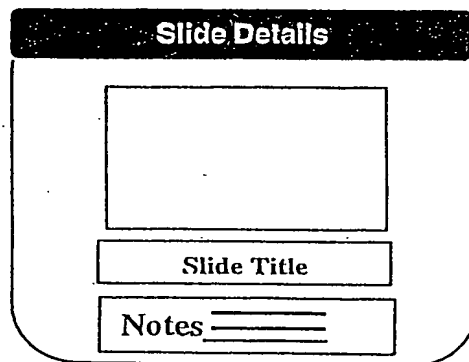


FIG. 4F

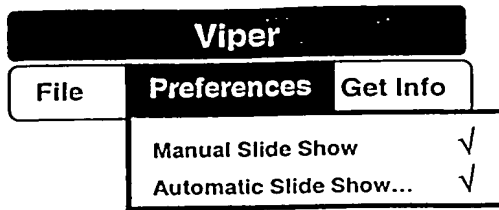


FIG. 4G

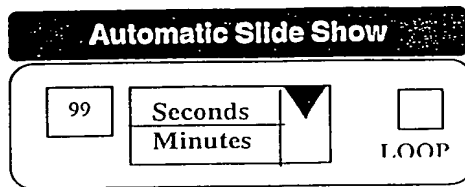


FIG. 4H

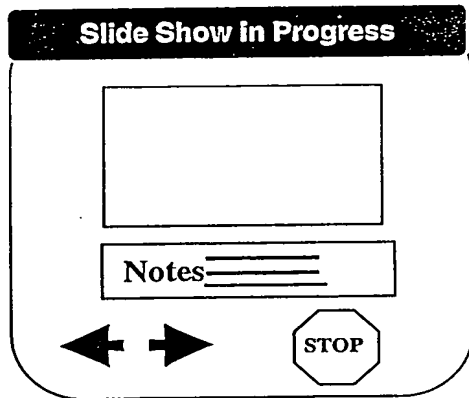


FIG. 4I

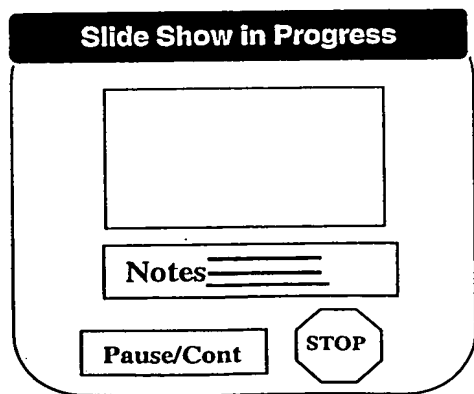


FIG. 4J

Register Addresses		Read (16 bits)	Write (16 bits)
High Address bits	Low Address (0-7)		
0x298000	00	ID, Version #, Status	Interrupt, Timer, Control
0x298000	02	Microcontroller Data Exchange Register	Microcontroller Data Exchange Register
0x298000	04	Microcontroller Data Transfer Status Register	Microcontroller Data Transfer Status Register
0x298000	06-0f	Reserved for Future	Reserved for Future

FIG. 5C

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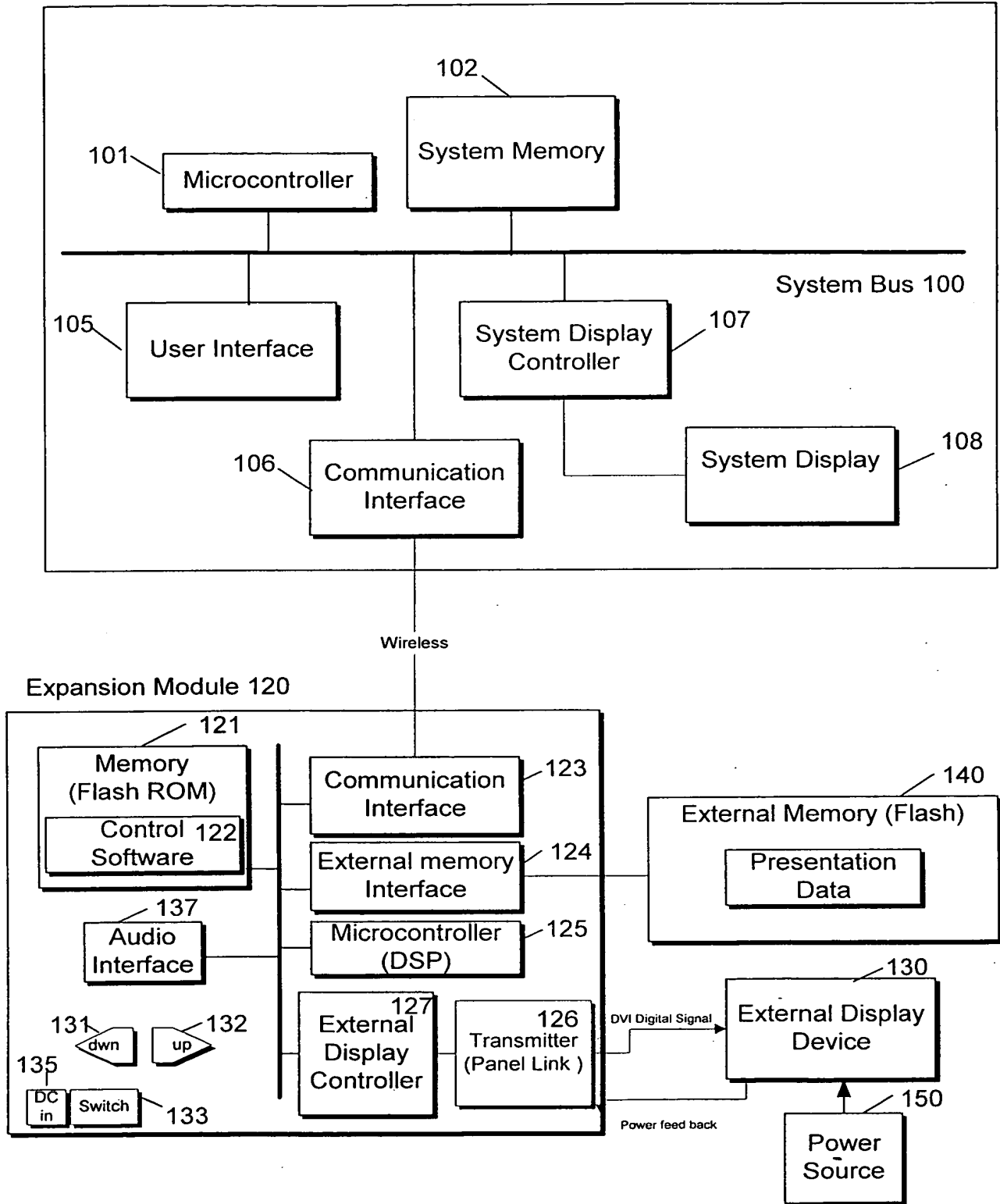


FIG. 6

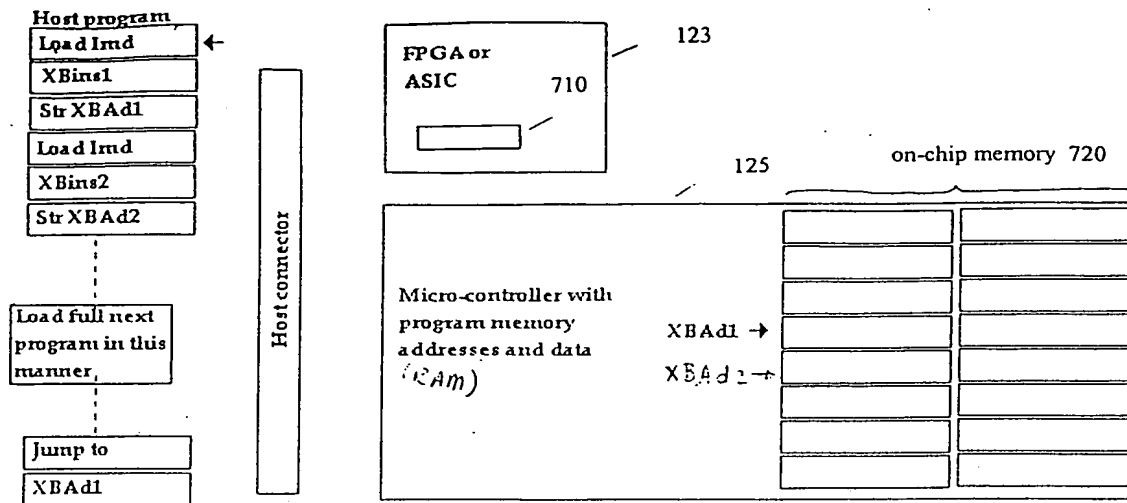


FIG. 7A

After reset

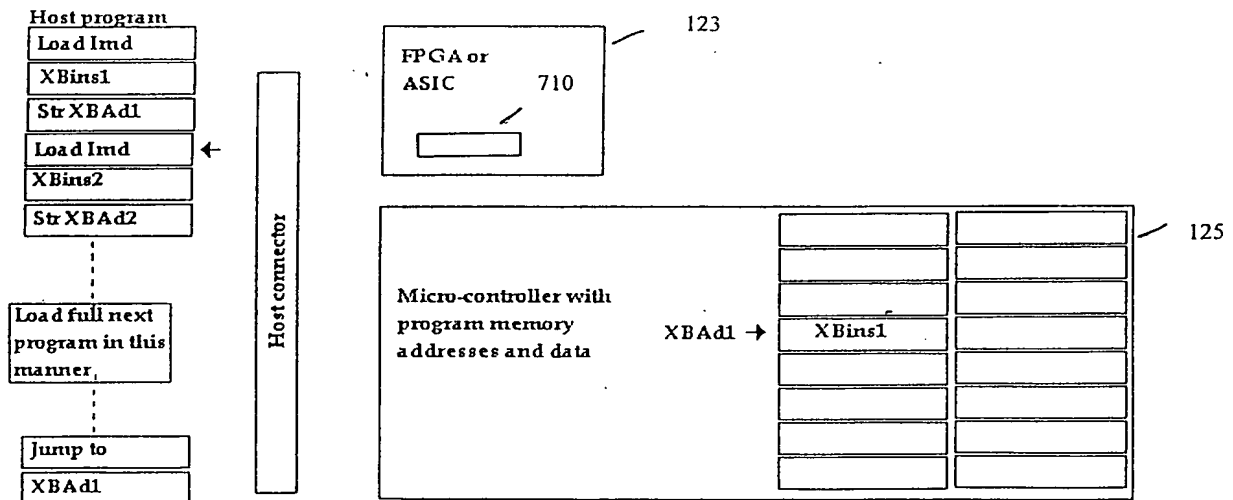


FIG. 7B

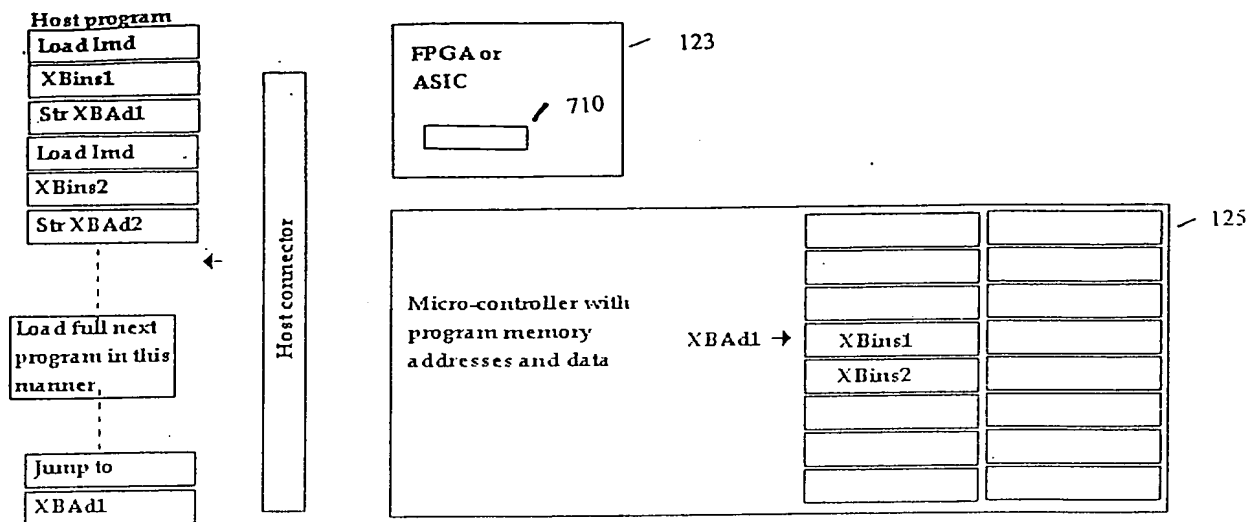


FIG. 7C

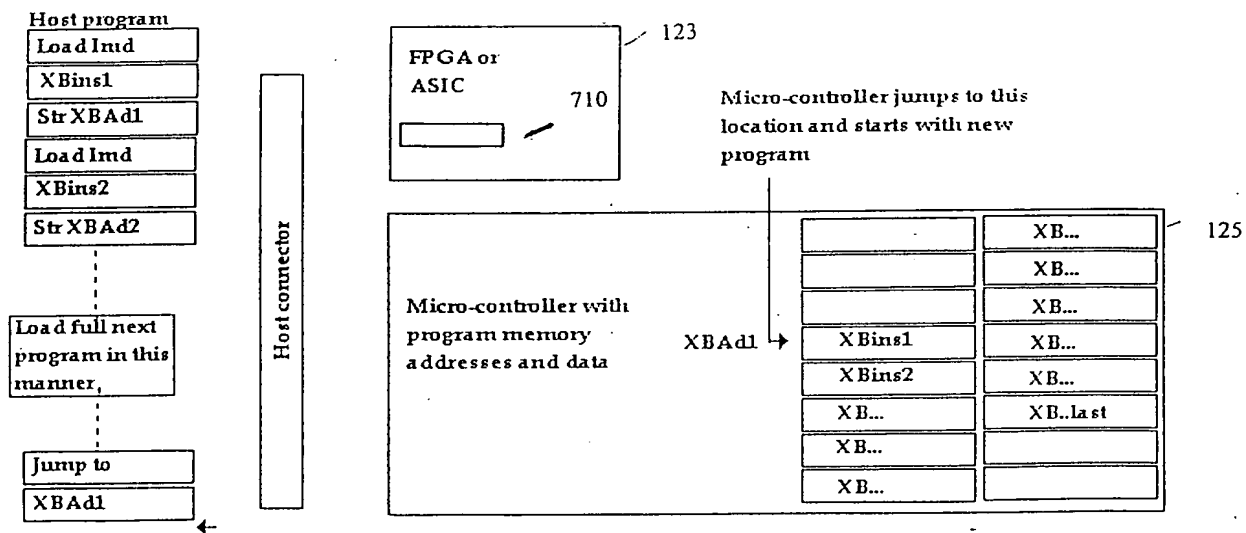


FIG. 7D

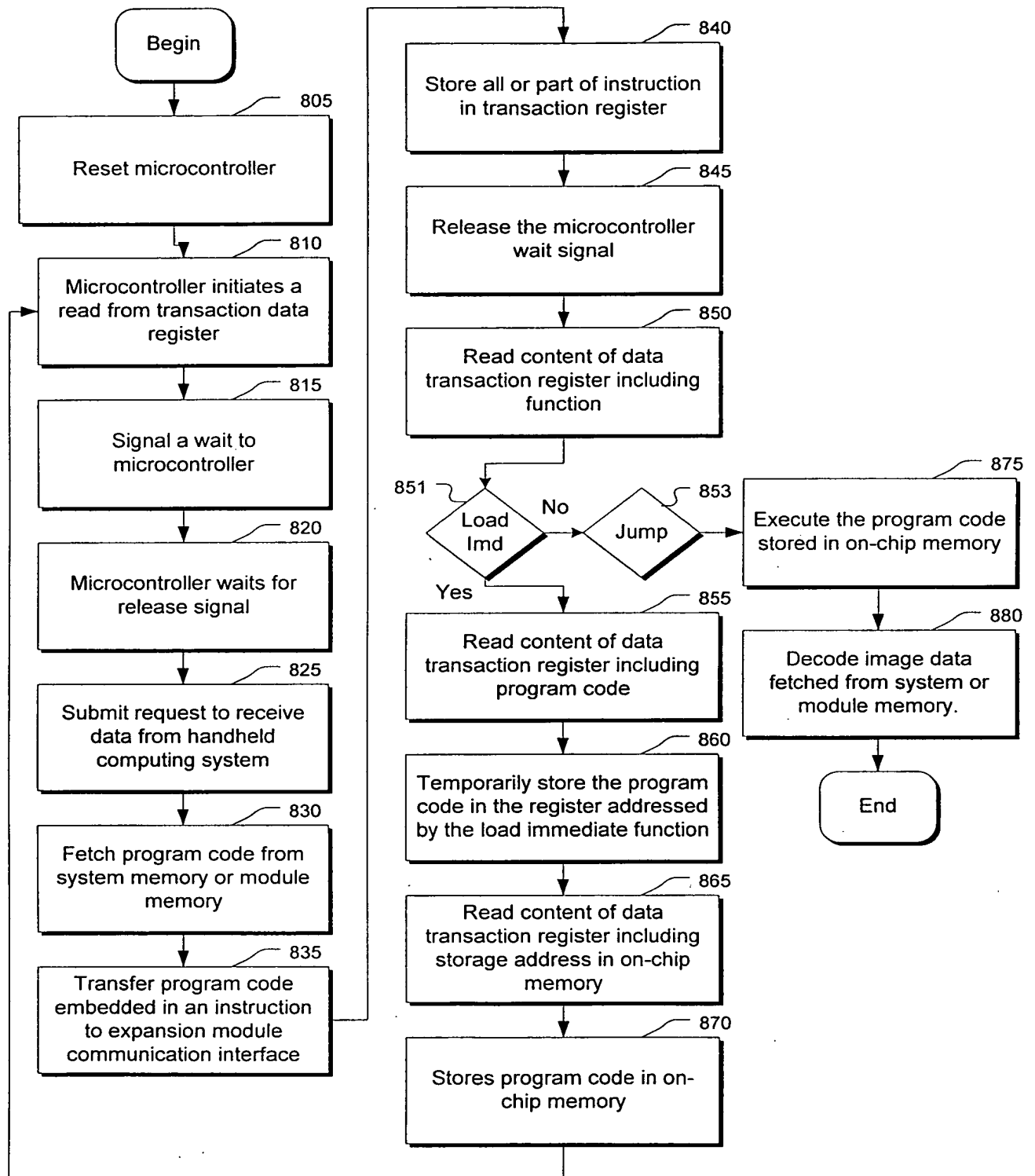


FIG. 8

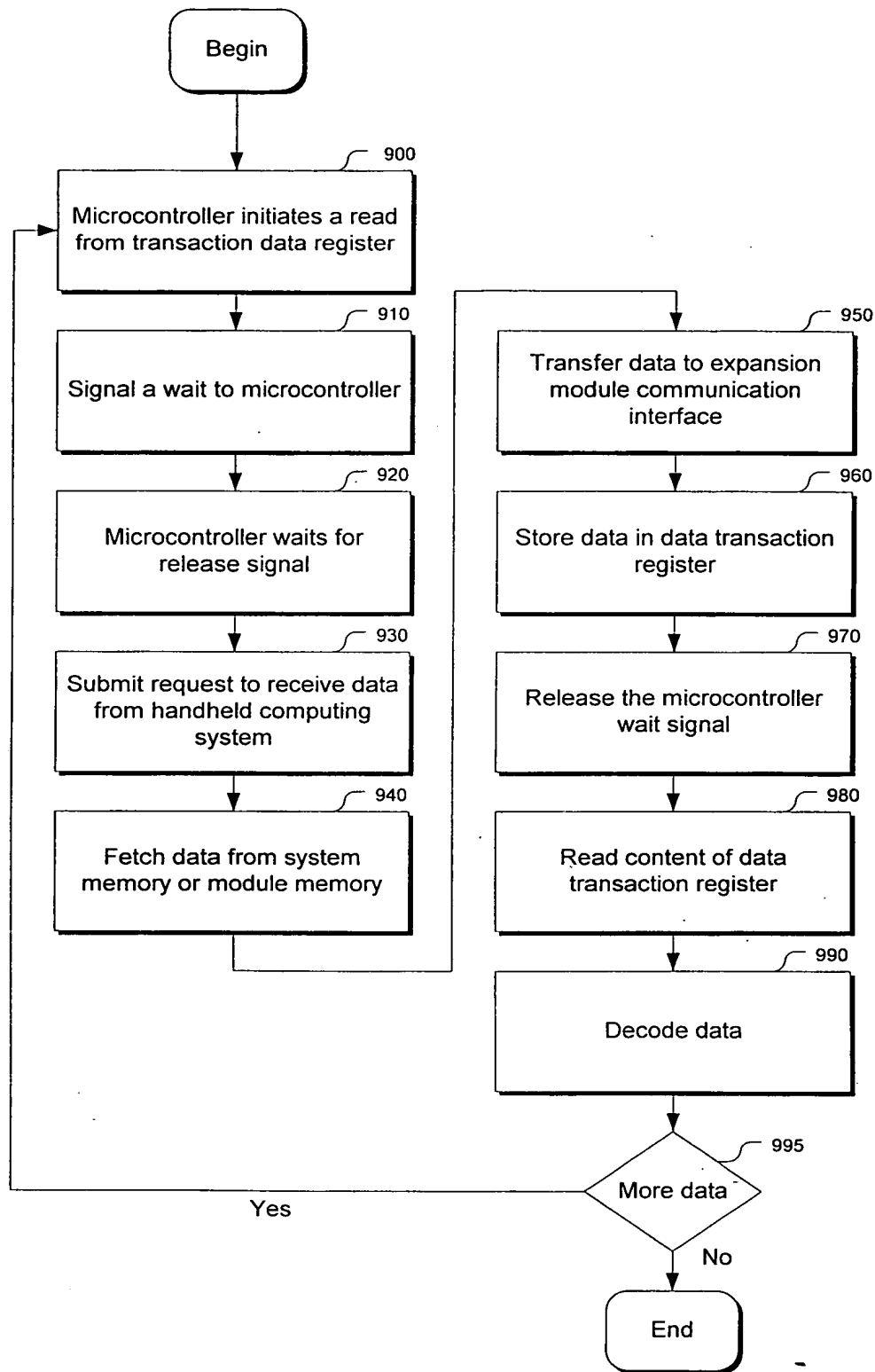


FIG. 9